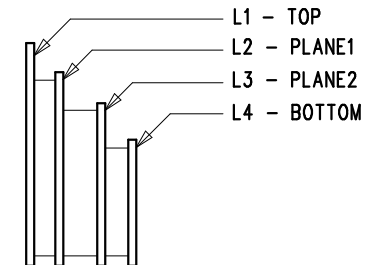


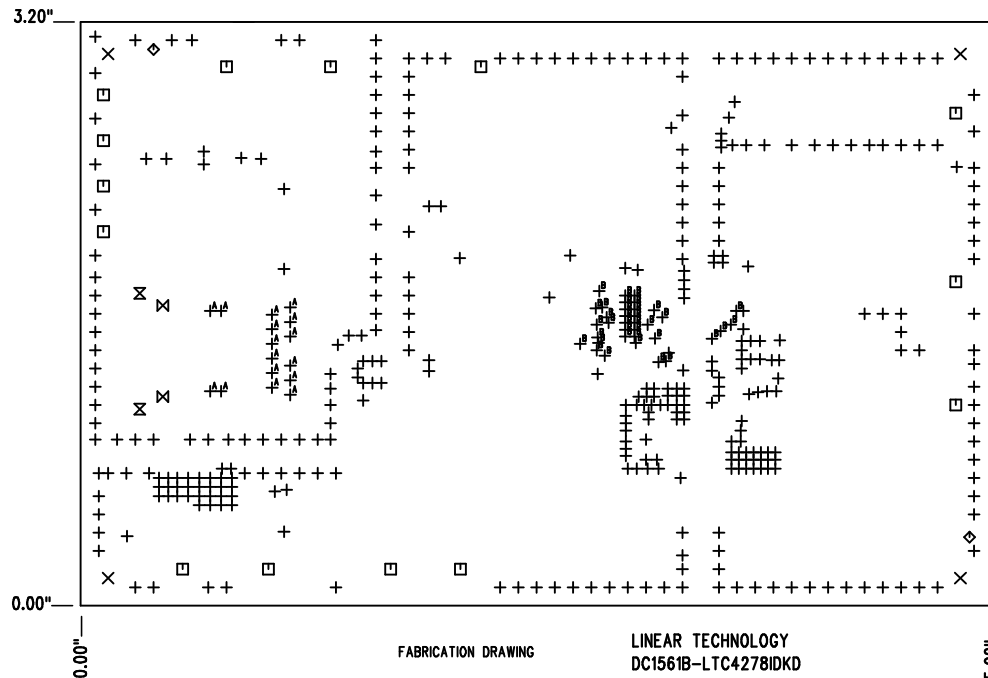
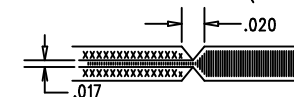
REVISION HISTORY				
ECO	REV	DESCRIPTION	APPR	DATE
-	1	PRODUCTION	RYAN H.	02-02-12

## LAYER STRUCTURE



## NOTES: UNLESS OTHERWISE SPECIFIED


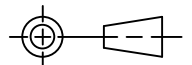
- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.  
-FINISHED THICKNESS TO BE 0.062" +/- .005"  
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.  
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.  
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.  
-GOLD IMMERSION BOTH SIDES.  
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.  
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



FABRICATION DRAWING

LINEAR TECHNOLOGY  
DC1561B-LTC4278IDKD  
HIGH EFFICIENCY PoE+PD  
AND SWITCHER WITH  
12V AUX SUPPORT  
DATE: 02-02-2012

SIZE	QTY	SYM	PLATED	TOL
0.015	362	+	YES	+/-0.003
0.187	4	X	NO	+/-0.003
0.094	14	□	YES	+/-0.003
0.07	2	◇	NO	+/-0.003
0.062	2	⊗	YES	+/-0.003
0.128	2	⊗	NO	+/-0.003
0.036	16	⊕ <sup>A</sup>	YES	+/-0.003
0.01	36	⊕ <sup>B</sup>	YES	+/-0.003

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON ANGLE ±° 0.XX" = ±0.01" 0.XXX" = ±0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994	APPROVALS		 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY
	PCB DES.	A. K.	
THIRD ANGLE PROJECTION	APP. ENG.	RYAN H.	TITLE: FABRICATION DRAWING HIGH EFFICIENCY PoE+PD AND SWITCHER WITH 12V AUX SUPPORT
			SIZE N/A
			IC NO. LTC4278IDKD DEMO CIRCUIT 1561B
DO NOT SCALE DRAWING	SCALE: NONE	FILENAME: DC1561B-1.PCB	REV. 1 SHT 1 of 1